

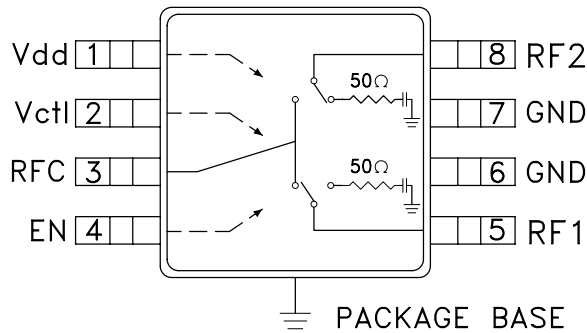
HIGH ISOLATION SPDT NON-REFLECTIVE SWITCH, DC - 4.0 GHz

Typical Applications

The HMC349MS8G is ideal for:

- Basestation Infrastructure
- MMDS & 3.5 GHz WLL
- CATV/CMTS
- Test Instrumentation

Functional Diagram



Features

- High Isolation: 70 dB @ 1 GHz
57 dB @ 2 GHz
- Single Positive Control: 0/+5V
- +52 dBm Input IP3
- Non-Reflective Design
- All Off State
- Ultra Small MS8G SMT Package: 14.8 mm²

General Description

The HMC349MS8G is a high isolation non-reflective DC to 4 GHz GaAs MESFET SPDT switch in a low cost 8 lead MSOP8G surface mount package with an exposed ground paddle. The switch is ideal for cellular/PCS/3G basestation applications yielding 50 to 60 dB isolation, low 0.8 dB insertion loss and +52 dBm input IP3. Power handling is excellent up through the 3.5 GHz WLL band with the switch offering a P1dB compression point of +31 dBm. On-chip circuitry allows a single positive voltage control of 0/+5 Volts at very low DC currents. An enable input (EN) set to logic high will put the switch in an "all off" state.

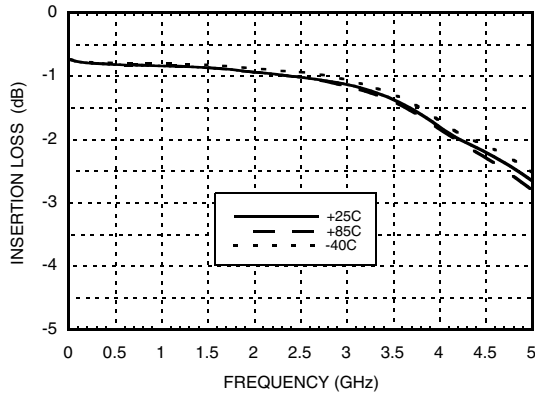
Electrical Specifications, $T_A = +25^\circ C$, $V_{ctl} = 0/+5 V_{dc}$, $V_{dd} = +5 V_{dc}$, 50 Ohm System

Parameter	Frequency	Min.	Typ.	Max.	Units
Insertion Loss	DC - 1.0 GHz		0.8	1.1	dB
	DC - 2.0 GHz		0.9	1.2	dB
	DC - 3.0 GHz		1.2	1.5	dB
	DC - 4.0 GHz		1.8	2.1	dB
Isolation (RFC to RF1/RF2)	DC - 1.0 GHz	60	70		dB
	DC - 2.0 GHz	54	57		dB
	DC - 3.0 GHz	45	50		dB
	DC - 4.0 GHz	42	47		dB
Return Loss (On State)	DC - 1.0 GHz		23		dB
	DC - 2.0 GHz		18		dB
	DC - 3.0 GHz		13		dB
	DC - 4.0 GHz		8		dB
Return Loss (Off State)	0.5 - 2.0 GHz		20		dB
	0.5 - 3.0 GHz		17		dB
	0.5 - 4.0 GHz		14		dB
Input Power for 1 dB Compression	0.25 - 4.0 GHz	27	31		dBm
Input Third Order Intercept (Two-Tone Input Power = +7 dBm Each Tone)	0.25 - 1.0 GHz		53		dBm
	1.0 - 2.0 GHz		50		dBm
	2.0 - 3.0 GHz		49		dBm
	3.0 - 4.0 GHz		47		dBm
Switching Speed	DC - 4.0 GHz				
		tRISE, tFALL (10/90% RF)		40	ns
		tON, tOFF (50% CTL to 10/90% RF)		120	ns

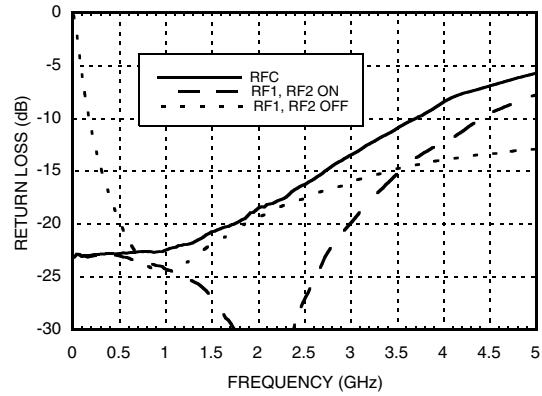
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Insertion Loss

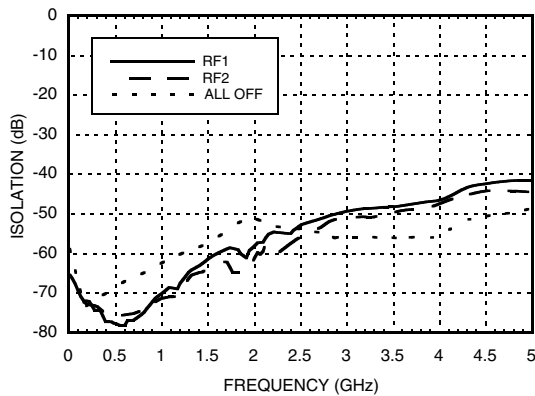


Return Loss

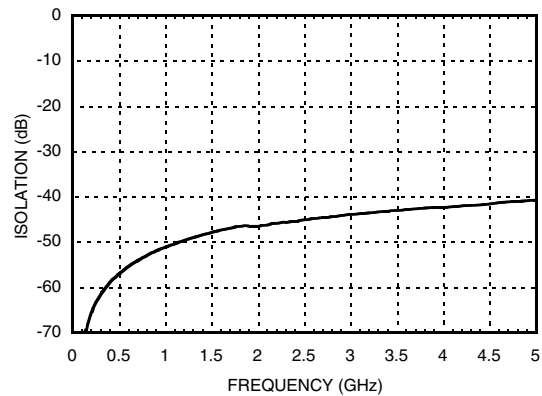


Note: RFC is reflective in "all off" state.

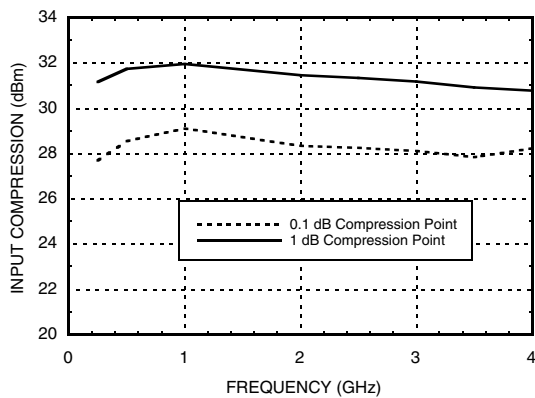
Isolation Between Ports RFC and RF1 / RF2



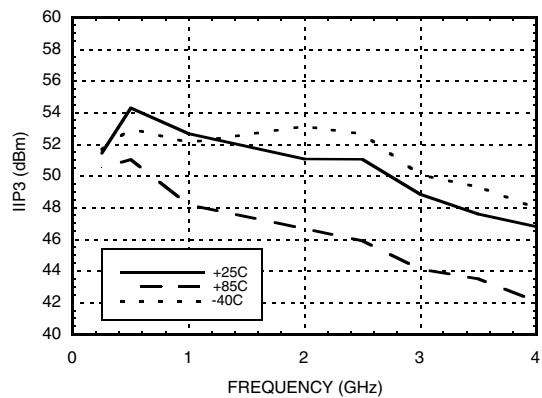
Isolation Between Ports RF1 and RF2



0.1 and 1 dB Input Compression Point



Input Third Order Intercept Point



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Absolute Maximum Ratings

RF Input Power (Vctl = 0V/+5V) (0.25 - 4 GHz)	+30 dBm (T = +85 °C)
Supply Voltage Range (Vdd)	+7 Vdc
Control Voltage Range (Vctl)	-1V to Vdd +1V
Hot Switch Power Level (Vdd = +5V)	+30 dBm
Channel Temperature	150 °C
Continuous Pdiss (T = 85 °C) (derate 12 mW/°C above 85 °C)	0.75 W
Thermal Resistance	87 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C

Note: DC blocking capacitors are required at ports RFC, RF1 and RF2. Their value will determine the lowest transmission frequency.

TTL/CMOS Control Voltages

State	Bias Condition
Low	0 to +0.8 Vdc @ <1 μA Typical
High	+2.0 to +5.0 Vdc @ 30 μA Typical

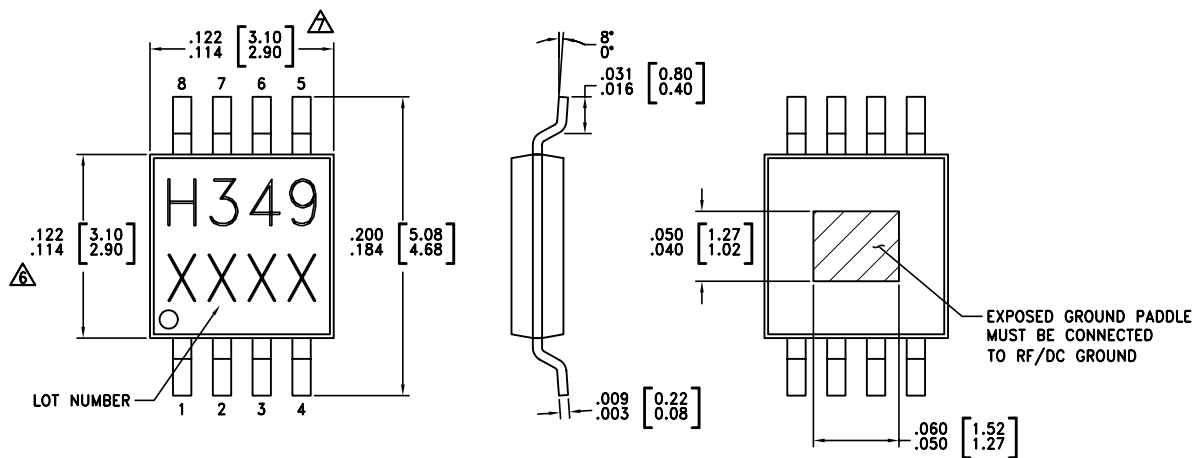
Truth Table

Control Input		Signal Path State	
Vctl	EN	RFC - RF1	RFC - RF2
Low	Low	OFF	ON
High	Low	ON	OFF
Low	High	OFF	OFF
High	High	OFF	OFF

Bias Voltage & Current

Vdd Range = +5.0 Vdc ± 10%		
Vdd (Vdc)	Idd (Typ.) (mA)	Idd (Max.) (mA)
+5.0	2.3	5.0

Outline Drawing



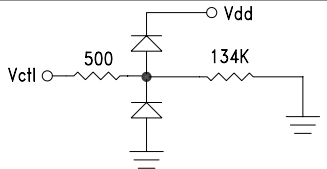
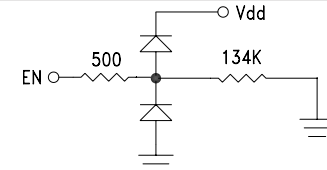

NOTES:

- PACKAGE BODY MATERIAL: LOW STRESS INJECTION MOLDED PLASTIC SILICA AND SILICON IMPREGNATED.
- LEAD AND GROUND PADDLE MATERIAL: COPPER ALLOY
- LEAD AND GROUND PADDLE PLATING: Sn/Pb SOLDER
- DIMENSIONS ARE IN INCHES [MILLIMETERS].
- △ DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.15mm PER SIDE.
- △ DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.25mm PER SIDE.
- ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.

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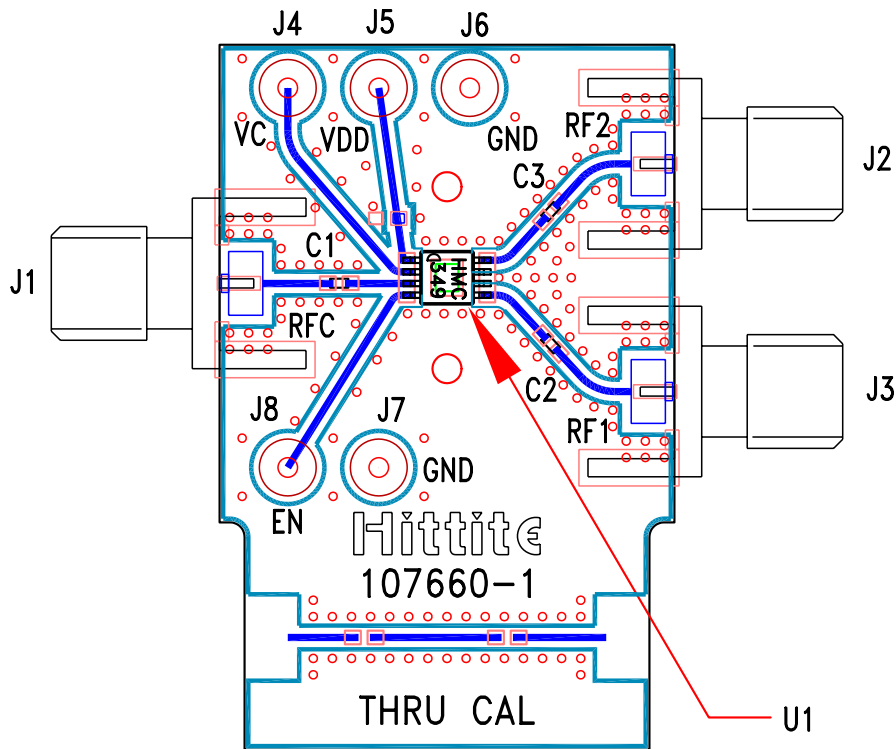
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Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1	Vdd	Supply Voltage.	
2	Vctl	Control input. See truth and control voltage tables.	
3, 5, 8	RFC, RF1, RF2	These pins are DC coupled and matched to 50 Ohms. Blocking capacitors are required.	
4	EN	Enable. See truth and control voltage tables.	
6, 7	GND	Package bottom must also be connected to PCB RF ground.	

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Evaluation PCB



List of Material for Evaluation PCB 107662*

Item	Description
J1 - J3	PC Mount SMA RF Connector
J4 - J8	DC Pin
C1 - C3	100 pF Capacitor, 0402 Pkg.
U1	HMC349MS8G SPDT Switch
PCB**	107660 Evaluation PCB
** Circuit Board Material: Rogers 4350	

The circuit board used in the final application should be generated with proper RF circuit design techniques. Signal lines at the RF port should have 50 ohm impedance and the package ground leads and backside ground slug should be connected directly to the ground plane similar to that shown above. The evaluation circuit board shown above is available from Hittite Microwave Corporation upon request.

* Reference this number when ordering complete evaluation PCB.

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NON-REFLECTIVE SWITCH, DC - 4.0 GHz***

Notes: